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FAX

Date:

August 4, 2006

Pages (including cover): 4

To:

Issue/Publication Fee Branch

Fax: 571-273-2885

Ph:

From:

Jeffrey L. Costellia, Reg. No. 35,483

Docket No. 740756-2063

Message:

The following documents are being presented for facsimile filing in the United States Patent and Trademark Office:

- 1. Notice of Allowance and Issue Fee Transmittal Part B (PTOL-85)
- 2. Correction of Prior Filed Information Disclosure Statement
- 3. Comments on Statement of Reasons for Allowance
- Permission to charge the Deposit Account No. 19-2380 in the amount of \$135.00 representing \$120.00 for Issue Fee and \$15.00 for Advance Order of 5 Patent Copies.

In re Patent Application of

Inventor(s): Shunpei YAMAZAKi et al.

Application No. 09/436,984 Filed: November 9, 1999

FOR: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Due Date: August 4, 2006 Docket Number: 740756-2063

JLC/plp

CERTIFICATE OF FACSIMILE TRANSMISSION [37 CFR 1.8(a)]

I hereby certify that this correspondence is being facsimile transmitted to the USPTO at 571-273-2885, on August 4, 2006

Signature:

Name:

Michelle Duvall



Docket No. 740756-2063

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Confirmation No.: 7375
Shunpei YAMAZAKI et al.)	
Application No. 09/436,984)	Examiner: William D. Coleman
Filed:	November 9, 1999)	Group Art Unit: 2823
For:	SEMICONDUCTOR DEVICE AND)	Date: August 4, 2006
	MANUFACTURING METHOD THEREOF)	

COMMENTS ON STATEMENTS OF REASONS FOR ALLOWANCE

MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants respectfully request consideration of the following comments on the Statement of Reasons for Allowance provided by the Examiner in the Notice of Allowability mailed May 4, 2006. Specifically, Applicants acknowledge with appreciation the Examiner's allowance of claims 1-14, 31-55 and 60-83. It should be noted, however, that claims 1-14 and 31 had been previously withdrawn from consideration. Further, these claims were not amended to include the feature "wherein the pair of sidewalls do not overlap the pair of second impurity regions and third impurity regions" nor do these claims recite that "the pair of side walls overlap only the pair of first impurity regions." As a result, Applicants wish to make clear on the record that claims 1-14 and 31 do not expressly recite the feature noted by the Examiner in the Statement of Reasons for Allowance.

Respectfully submitted,

By:

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